

L Number	Hits	Search Text	DB	Time stamp
-	603	debug\$5 same embedd\$5 same system\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/07 18:01
-	137	(debug\$5 same embedd\$5 same system\$4) and cpu and bus and register and (logic same circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/07 18:02
-	7	(debug\$5 same embedd\$5 same system\$4) and cpu and bus and register and (logic same circuit) and (break adj point same detect\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/07 18:03
-	1	(debug\$5 same embedd\$5 same system\$4) and cpu and bus and register\$4 and (logic NEAR2 circuit\$4) and (break adj point same detect\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/07 18:04
-	7	(debug\$5 same embedd\$5 same system\$4) and cpu and bus and register\$4 and (logic NEAR5 circuit\$4) and (break adj point same detect\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 10:02
-	1093	717/124-129.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 10:03
-	22	717/124-129.ccls. and (debug\$5 same embedd\$5 same system\$4) and cpu and bus and register\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 13:59
-	1	6581191.pn. and (debug\$5 same embedd\$5 same system\$4) and cpu and bus and register and (logic same circuit) and (break adj point same detect\$4) and (debug\$5 same logic same circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:01
-	0	6581191.pn. and (debug\$5 same embedd\$5 same system\$4) and cpu and bus and register and (logic same circuit) and (break adj point same detect\$4) and (debug\$5 NEAR2 logic NEAR2 circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:02
-	0	6581191.pn. and (debug\$5 same embedd\$5 same system\$4) and cpu and bus and register and (logic same circuit) and (break adj point same detect\$4) and (debug\$5 NEAR3 logic NEAR3 circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:02
-	0	6581191.pn. and (debug\$5 same embedd\$5 same system\$4) and cpu and bus and register and (logic same circuit) and (break adj point same detect\$4) and (debug\$5 NEAR5 logic NEAR5 circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:02
-	1	6581191.pn. and (debug\$5 same embedd\$5 same system\$4) and cpu and bus and register and (logic same circuit) and (break adj point same detect\$4) and (debug\$5 NEAR10 logic NEAR10 circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:32
-	1	6581191.pn. and bus near4 register\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 17:20
-	0	(break adj point\$4 same detect\$4 same circuit\$4 same compar\$4) near5 multiplex\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 17:22

-	5	(break adj point\$4 same detect\$4 same circuit\$4 same compar\$4) and multiplex\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 17:22
-	239	break adj point\$4 same counter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 16:13
-	44	break adj point\$4 near3 counter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 16:13
-	1	(break adj point\$4 near3 counter) same detect same circuit\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 16:14
-	2	(break adj point\$4 near3 counter) same detect\$4 same circuit\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 16:15
-	16	(break adj point\$4 near3 counter) and detect\$4 same circuit\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 16:15
-	1555	method same debug\$4 same embedd\$4 samee micro adj (controller\$4) and program\$7 same logic same circuit\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:16
-	1555	method same debug\$4 same embedd\$4 samee micro adj (controller\$4) and (program\$7 same logic same circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:16
-	1	(method same debug\$4 same embedd\$4 same micro adj controller\$4) and (program\$7 same logic same circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:17
-	1	(method same debug\$4 same embedd\$4 same (micro adj controller\$4)) and (program\$7 same logic same circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:20
-	1	(method same debug\$4 same embedd\$4 same (micro adj controller\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:20
-	10	(debug\$4 same embedd\$4 same (micro adj controller\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:50
-	2964	program\$5 same debug\$4 same device\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:51
-	0	(program\$5 same debug\$4 same device\$4) near microcontrol\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:51

-	1705	program\$5 NEAR4 debug\$4 same device\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:52
-	497	program\$5 NEAR4 debug\$4 NEAR2 device\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:52
-	376	program\$5 NEAR2 debug\$4 NEAR2 device\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:52
-	217	program\$5 NEAR1 debug\$4 NEAR1 device\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:52
-	1	program\$5 NEAR1 (debug\$4 NEAR1 logic\$4 near1 circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:54
-	1	program\$5 NEAR (debug\$4 NEAR logic\$4 near circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:54
-	1	program\$5 NEAR4 (debug\$4 NEAR logic\$4 near circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:54
-	23	program\$5 NEAR4 (debug\$4 NEAR4 logic\$4 near4 circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:55
-	8	program\$5 NEAR2 (debug\$4 NEAR2 logic\$4 near2 circuit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 17:55